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PTO/SB/08A (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 1 of 4

Complete if Known

Application Number	10/725,888
Filing Date	December 2, 2003
First Named Inventor	William J. Borland
Art Unit	2841
Examiner Name	HOA C. NGUYEN
Attorney Docket Number	EL0497 US NA

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
HW		US- 4,190,854	02-26-1980	Redfern	
HW		US- 6,317,023	11-13-2001	Felten	
HW		US- 6,631,551	10-14-2003	Bowles et al.	
HW		US- 2002/0179329 A1	12-05-2002	Fukuoka et al.	
HW		US- 2003/0154592 A1	08-21-2003	Felten	
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FOREIGN PATENT DOCUMENTS

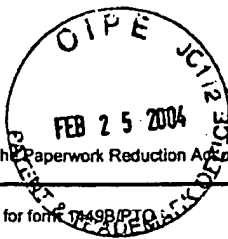
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
		JP 2003-92460	05-31-2002			
		JP 2-153589	06-13-1990	Ibiden KK		
		JP 2001-168491	12-09-1999			

Examiner Signature	Hoanguyen	Date Considered	4/5/06
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
felw		JOHN FELTEN AND SAUL FERGUSON, Embedded Ceramic Resistors and Capacitors for PWB, IP Printed Circuit Expo, San Diego, CA, April 2000.	<input type="checkbox"/>
		JOHN J. FELTEN AND SAUL FERGUSON, Ceramic Resistors and Capacitors Embedded in PWB, IPC, San Diego, April 5, 2000.	<input type="checkbox"/>
		JOHN J. FELTEN AND SAUL FERGUSON, Ceramic Resistors and Capacitors Embedded in PWB, IMAPS, Denver, April 29, 2000.	<input type="checkbox"/>
		JOHN J. FELTEN AND WILLIAM J. BORLAND, Embedded Ceramic Passives in PWB: Process Development, IPC Printed Circuit Expo, Anaheim, CA, April 2001.	<input type="checkbox"/>
		JOHN J. FELTEN AND WILLIAM J. BORLAND, Ceramic Resistors and Capacitors Embedded in PWB's, IPC Expo, April 3, 2001.	<input type="checkbox"/>
		WILLIAM BORLAND, Designing for Embedded Passives, Printed Circuit Design, August 2001.	<input type="checkbox"/>

Examiner
Signature

Hoa Nguyen

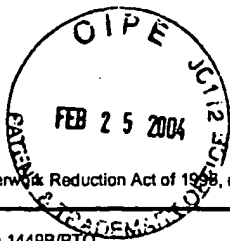
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Filing Date December 2, 2003

First Named Inventor William J. Borland

Art Unit 2841

Examiner Name HOA C. NGUYEN

Attorney Docket Number EL0497 US NA

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
HL		WILLIAM BORLAND, JOHN J. FELTEN, Thick Film Ceramic Capacitors and Resistors inside Printed Circuit Boards, 34th International Symposium on Microelectronics (IMAPS), Oct. 9-11th, 2001, Baltimore, MD.	<input type="checkbox"/>
		JOHN FELTEN, RICHARD SNOGREN, JIMING ZHOU, Embedded Ceramic Resistors and Capacitors in PWB: Process and Performance, Fall IPC Meeting, October 11, 2001, Orlando, FL.	<input type="checkbox"/>
		WILLIAM J. BORLAND AND SAUL FERGUSON, Embedded Passive Components in Printed Wiring Boards, a Technology Review, To be published in CircuiTree Magazine, 2001.	<input type="checkbox"/>
		JIMING ZHOU, JOHN D. MYERS AND JOHN J. FELTEN, Embedded Passives Technology for PCBs: Materials, Design, and Process, IMAPS 2002 Conference, Denver, CO, Sept. 4-6, 2002.	<input type="checkbox"/>
		JOHN J. FELTEN, Electronic Circuits World Convention, Paper number IPC31, Advanced Embedded Passives Technologies - Putting Ceramic Components into Organic PWBs.	<input type="checkbox"/>
		WILLIAM BORLAND, JOHN J. FELTEN, SAUL FERGUSON, ALTON B. JONES AND ANGELA A. LAWRENCE, Embedded Singulated Ceramic Passives in Printed Wiring Boards, IMAPS Advanced Technology Workshop on Passive Integration, Ogunquit, ME, June 19-21, 2002.	<input type="checkbox"/>

Examiner
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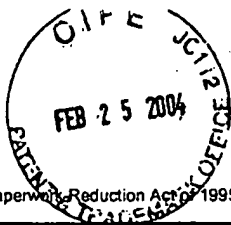
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First Named Inventor

William J. Borland

Art Unit

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Examiner Name

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Feb		DR. KIM FJELDSTED AND STACEY L. CHASE, EMBEDDED PASSIVES: LASER TRIMMED RESISTORS, CIRCUITREE, Volume 70, March 2002.	<input type="checkbox"/>
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